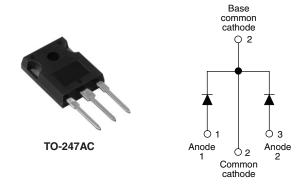


Vishay High Power Products

Schottky Rectifier, 2 x 20 A



PRODUCT SUMMARY				
I _{F(AV)}	2 x 20 A			
V_{R}	15 V			
I _{RM}	600 mA at 100 °C			

FEATURES

- 125 °C T_J operation (V_R < 5 V)
- · Center tap module
- Optimized for OR-ing applications
- · Ultra low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- · Designed and qualified for industrial level

DESCRIPTION

The 40L15CWPbF center tap Schottky rectifier module has been optimized for ultra low forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	40	Α		
V _{RRM}		15	V		
I _{FSM}	$t_p = 5 \mu s sine$	700	Α		
V _F	19 Apk, T _J = 125 °C (per leg, typical)	0.25	V		
T _J		- 55 to 125	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	40L15CW	UNITS
Maximum DC reverse voltage	V_R	T ₁ = 100 °C	15	V
Maximum working peak reverse voltage	V_{RWM}	1j=100 C	15	V

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	per leg		50 % duty cycle at T_C = 86 °C, rectangular waveform		20	
See fig. 5	per device	I _{F(AV)}			40	Α
Maximum peak one cycle	or log		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	700	
non-repetitive surge current per leg I _{FSM} See fig. 7		IFSM	10 ms sine or 6 ms rect. pulse		330	
Non-repetitive avalanche ener	rgy per leg	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 2 \text{A}, L = 5 \text{mH}$		10	mJ
Repetitive avalanche current p	per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \text{ x } V_B$ typical		2	А

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
	V _{FM} ⁽¹⁾	19 A	T _J = 25 °C	-	0.41	V
Maximum forward voltage drop per leg		40 A		-	0.52	
See fig. 1		19 A	T _J = 125 °C	0.25	0.33	
		40 A		0.37	0.50	
Reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V Dated V	-	10	mA
See fig. 2	'RM '''	T _J = 100 °C	V_R = Rated V_R	-	600	IIIA
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		0.1	82	V
Forward slope resistance	r _t			7.	.6	mΩ
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		-	2000	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8	-	nΗ
Maximum voltage rate of change	dV/dt	Rated V _R 10 000		000	V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction temperature range	TJ		- 55 to 125	°C	
Maximum storage temperature range	T _{Stg}		- 55 to 150	10	
Maximum thermal resistance, junction to case per leg	Б	DC operation See fig. 4	1.4		
Maximum thermal resistance, junction to case per package	- R _{thJC}	DC operation	0.7	°C/W	
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.24		
Approximate weight			6	g	
Approximate weight			0.21	OZ.	
minimum		Non-lubricated threads	6 (5)	kgf · cm	
Mounting torque maximun	n	Non-iublicateu tilleaus	12 (10)	(lbf ⋅ in)	
Marking device		Case style TO-247AC (JEDEC)	40L1	5CW	



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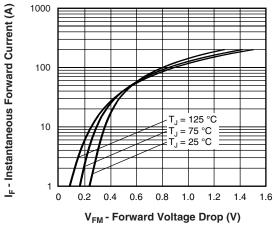


Fig. 1 - Maximum Forward Voltage Drop Characteristics

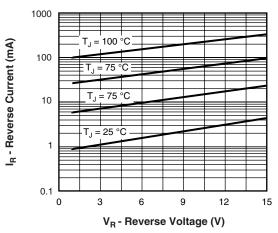


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

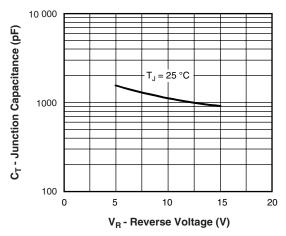


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

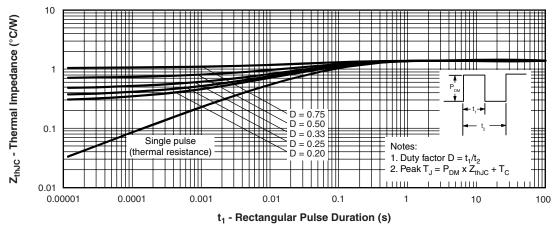


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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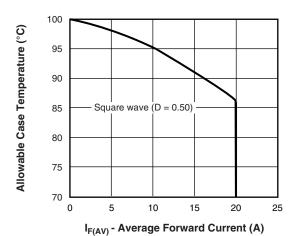


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

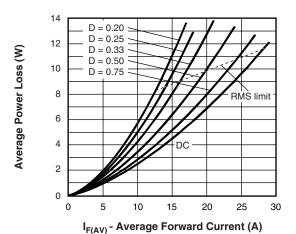


Fig. 6 - Forward Power Loss Characteristics

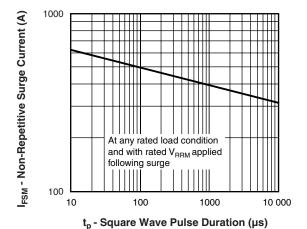


Fig. 7 - Maximum Non-Repetitive Surge Current

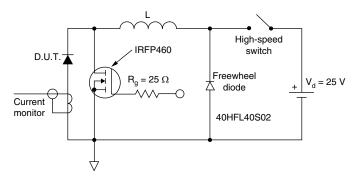


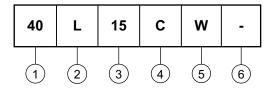
Fig. 8 - Unclamped Inductive Test Circuit



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ORDERING INFORMATION TABLE

Device code



1 - Current rating (40 = 40 A)

2 - Schottky "L" series

3 - Voltage code (15 = 15 V)

- Circuit configuration:

C = Common cathode

5 - Package:

W = TO-247

6 - • None = Standard production

• PbF = Lead (Pb)-free

Tube standard pack quantity: 25 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions	http://www.vishay.com/doc?95223			
Part marking information	http://www.vishay.com/doc?95226			

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